Application No.: 10/781,338

Filing Date: February 17, 2004

REMARKS

Claims 36-39 are pending. Claims 1-35 have been canceled without prejudice or disclaimer. The Applicants have herein amended Claim 36 and added new Claims 38 and 39.

Allowable Claims 36 and 37

The Applicants note that the Examiner indicated that Claims 36 and 37 would be allowable if rewritten in independent form. The Applicants have rewritten Claim 36. The Applicants thus submit that Claim 36 is allowable and that Claim 37 is allowable at least because of its dependence on Claim 36.

New Claims 38 and 39

The Applicants note that new Claim 38 and 39 differ from allowable Claims 36 and 37 because Claim 38 recites "a memory channel" rather than "a remote memory channel" as recited in Claim 36. The Applicants believe that new Claims 38 and 39 are allowable over the art of record.

No Disclaimers or Disavowals

Although the present communication may include alterations to the application or claims, or characterizations of claim scope or referenced art, Applicant is not conceding in this application that previously pending claims are not patentable over the cited references. Rather, any alterations or characterizations are being made to facilitate expeditious prosecution of this application. Applicant reserves the right to pursue at a later date any previously pending or other broader or narrower claims that capture any subject matter supported by the present disclosure, including subject matter found to be specifically disclaimed herein or by any prior prosecution. Accordingly, reviewers of this or any parent, child or related prosecution history shall not reasonably infer that Applicant has made any disclaimers or disavowals of any subject matter supported by the present application.

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Co-Pending Applications of Assignee

Applicant wishes to draw the Examiner's attention to the following co-pending applications of the present application's assignee.

Serial Number	Title	Filed
12/415,208	REMOTE DIRECT MEMORY ACCESS FOR ISCSI	March 31, 2009
11/835,942	STORAGE CONTROLLER REDUNDANCY USING PACKET-BASED PROTOCOL TO TRANSMIT BUFFER DATA OVER REFLECTIVE MEMORY CHANNEL	August 8, 2007
12/325,974	NETWORK RECEIVE INTERFACE FOR HIGH BANDWIDTH HARDWARE-ACCELERATED PACKET PROCESSING	December 1, 2008
12/140,101	SYSTEM AND METHODS FOR HIGH RATE HARDWARE-ACCELERATED NETWORK PROTOCOL PROCESSING	June 16, 2008

Please charge any additional fees, including any fees for additional extension of time, or credit overpayment to Deposit Account No. 11-1410.

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: April 15, 2009 By: _/Ted M. Cannon/

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AMEND

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